

Confirmation No. 8030

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|             |  |                 |                             |
|-------------|--|-----------------|-----------------------------|
| Applicant:  | VAN SCHAIJK <i>et al.</i>                                  | Examiner:       | Hsieh, H.                   |
| Serial No.: | 10/574,030   | Group Art Unit: | 2811                        |
| Filed:      | March 27, 2006   | Docket No.:     | NL031167US1<br>(NXPS.442PA) |
| Title:      | TWO-TRANSISTOR MEMORY CELL AND METHOD FOR<br>MANUFACTURING |                 |                             |

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AMENDMENT AND RESPONSE WITH RCE

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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| Customer No.<br><b>65913</b> |
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Dear Sir:

In response to the Final Office Action dated April 2, 2009, please consider the following amendments and remarks.

A complete listing of the Claims, including any amendments, and the Remarks/Arguments follow.